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TDS Statement
PATENT
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Shuji MIYASAKA et al. Art Unit:
Serial No.: filed concurrently Examiner:
Filed : December 22, 2000
Title : SIGNAL PROCESSING DEVICE AND
SIGNAL PROCESSING METHOD

Commissioner of Patents and Trademarks
Washington, DC 20231

JCS99 U.S. PTO
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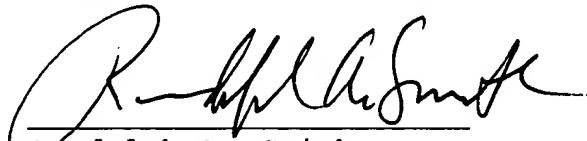

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to their duty of good faith and candor as set forth in 37 CFR 1.56(a), applicants request that the reference cited on attached form PTO-1449 (a copy of which is enclosed) be considered by the Examiner in connection with the search of the prior art required by 37 CFR 1.104.

This Statement is being filed under 37 CFR 1.97(b) prior to the mailing date of a first Office Action on the merits.

Respectfully submitted,



Randolph A. Smith

Reg. No. 32,548

Date: December 22, 2000

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